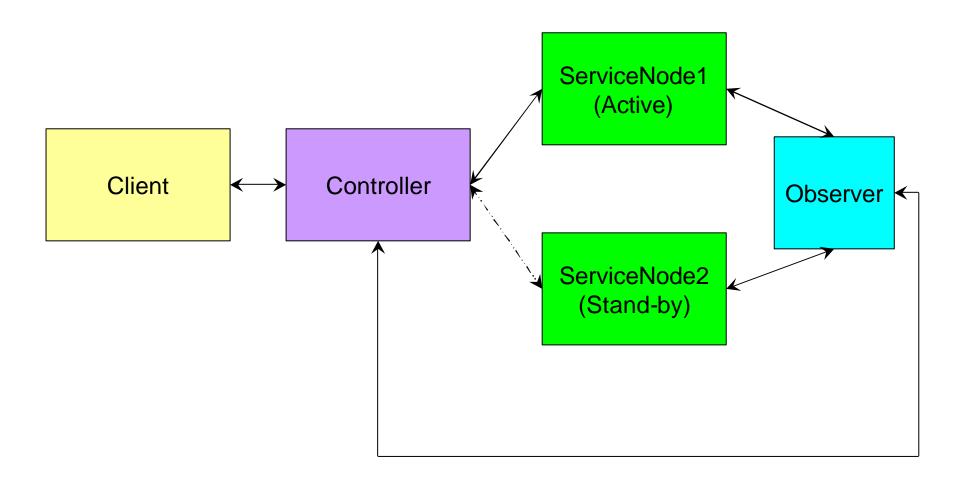
A simulation about High Availability

KISHI Yasuhiro

Logical architecture



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